



1/6

FIG. 1

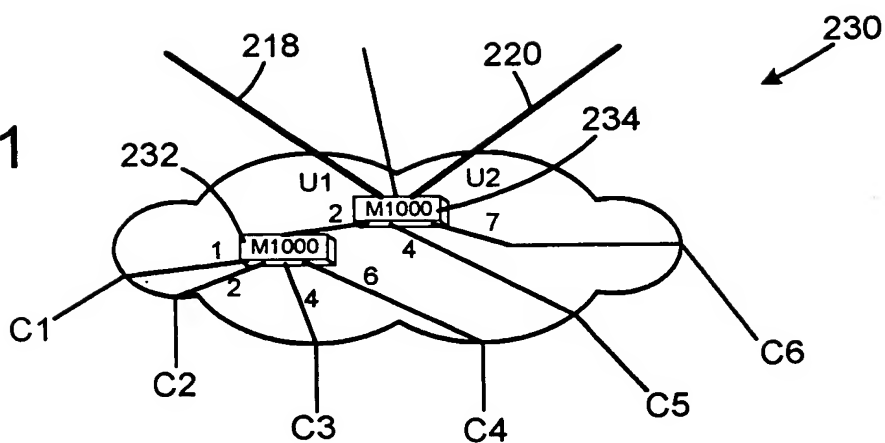


FIG. 2

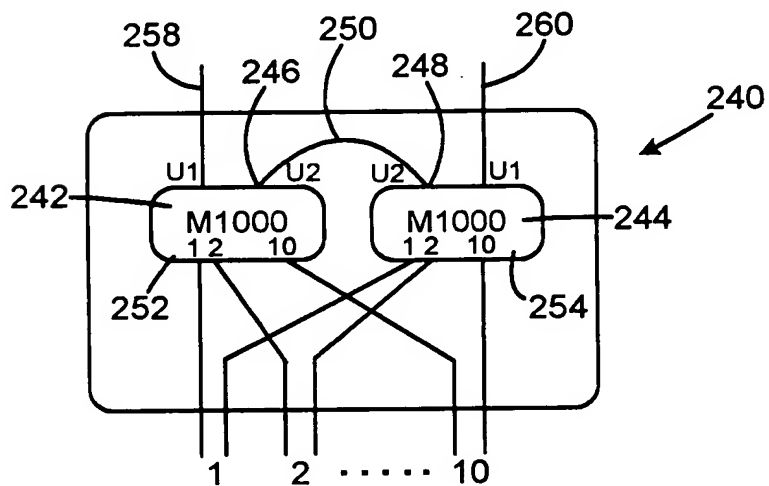


FIG. 3

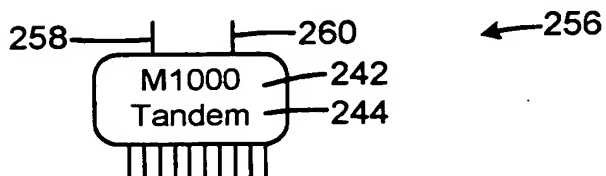


FIG. 4

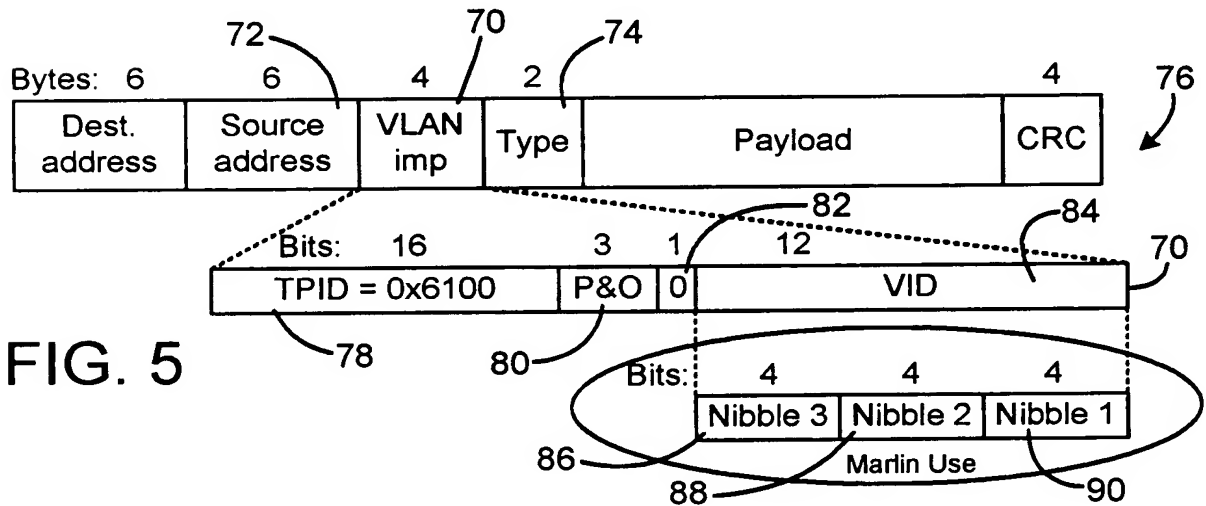
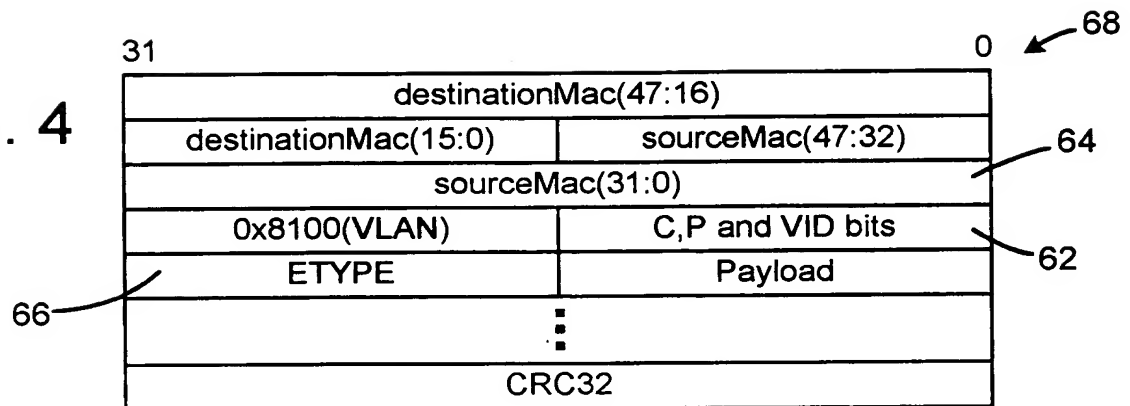


FIG. 5

FIG. 6

Address	Description
0x0	This level not addressed
0x1 - 0xA	Address for ports 1-10
0xB - 0xD	Reserved
0xE	Address for AUX port
0xF	Address for ring topology detection
Untagged	Address for OAM in unit

FIG. 7

Nibble	Valid values
Nibble 3	0x0 - 0xA, non zero only if Nibble 2 is non zero
Nibble 2	0x0 - 0xA
Nibble 1	0x1 - 0xA, 0xE, 0xF

Address	First Nibble	Second Nibble
0x0	This level not addressed	This level not addressed
0x1 - 0xA	Ring node 1-10	Port 1-10
0xB - 0xC	Ring node 11-12	Reserved
0xD	Reserved	Reserved
0xE	Reserved	Address for AUX port
0xF	Reserved	Address for topology detection
Untagged	Reserved	Address for OAM in unit

FIG. 8

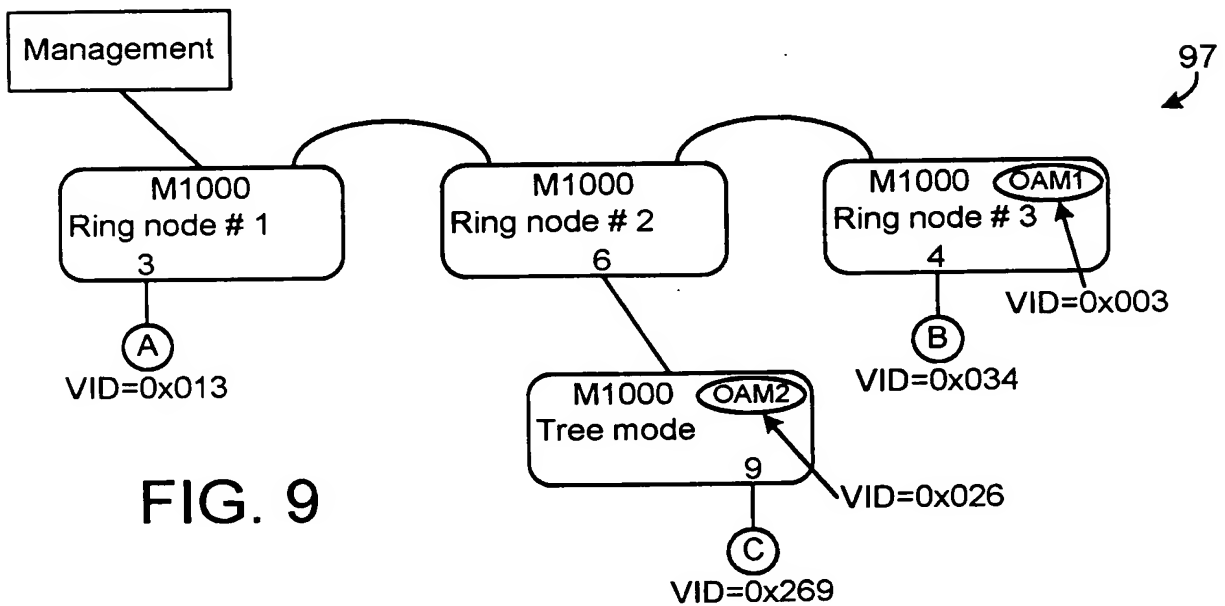


FIG. 9

Nibble	Valid values
Nibble 3	0x0 - 0xC, non zero only if Nibble 2 is non zero, 0xB or 0xC only if this field is ring node number
Nibble 2	0x0 - 0xC, 0xB or 0xc only if this field is ring node number
Nibble 1	0x1 - 0xA, 0xE, 0xF

FIG. 10

FIG. 11

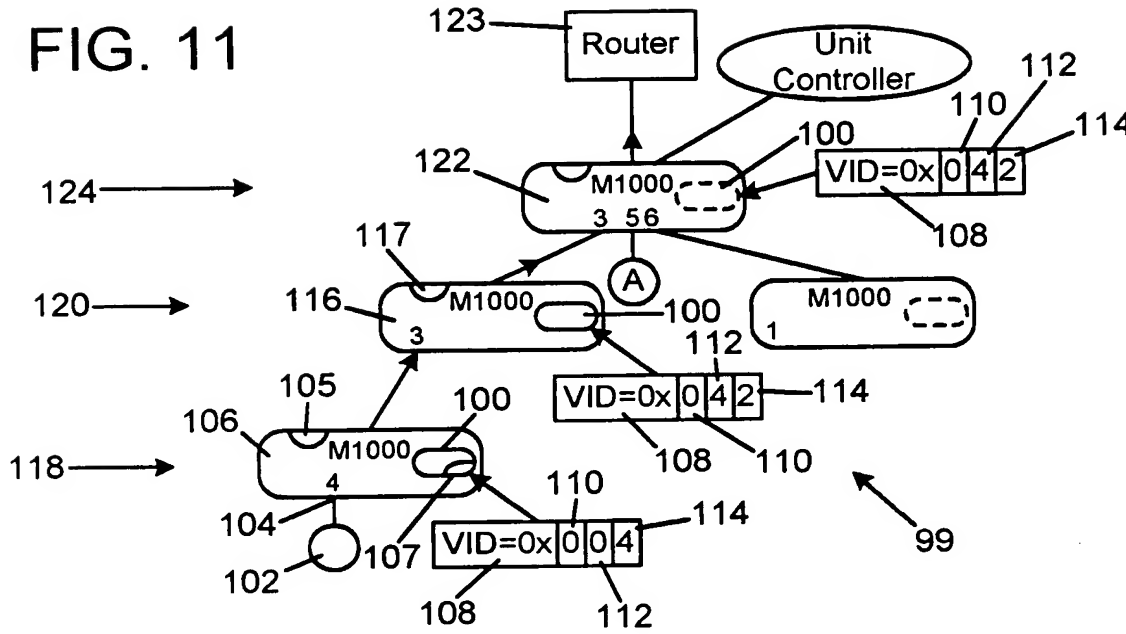


FIG. 12

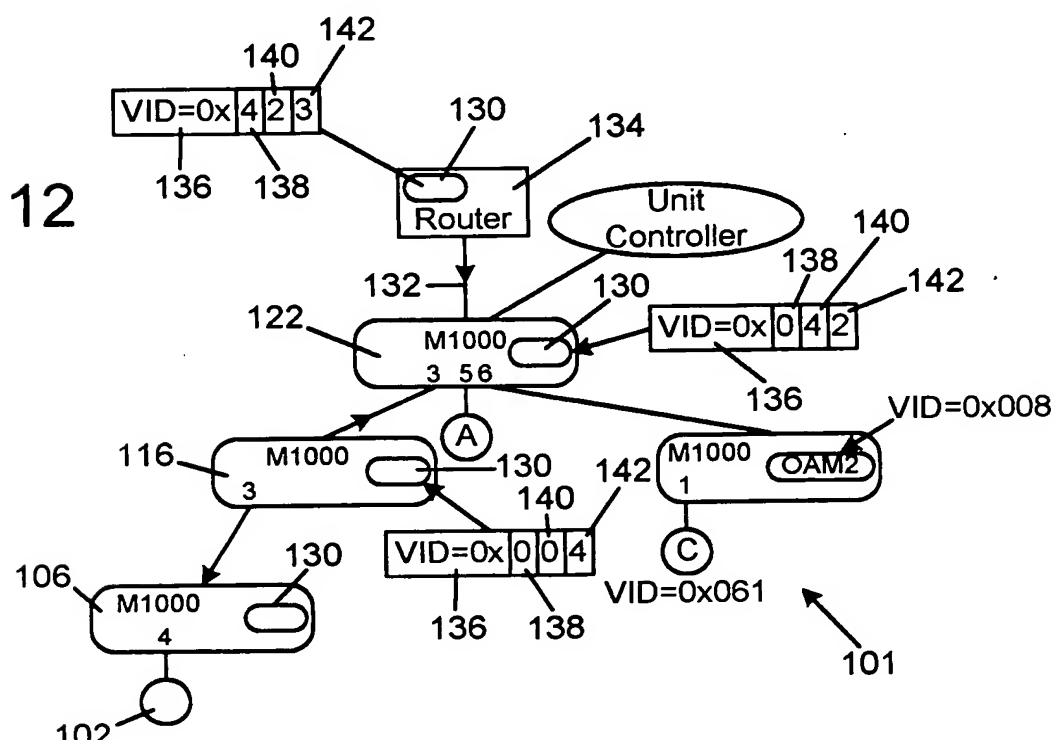


FIG. 13

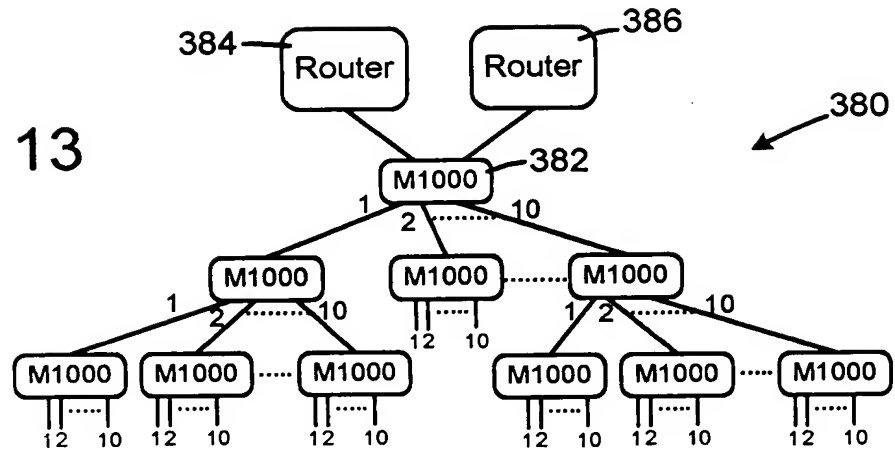


FIG. 14

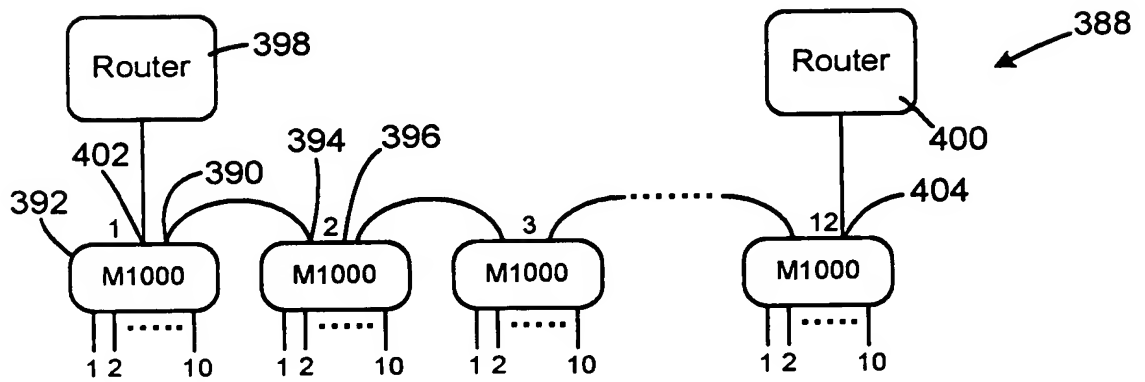
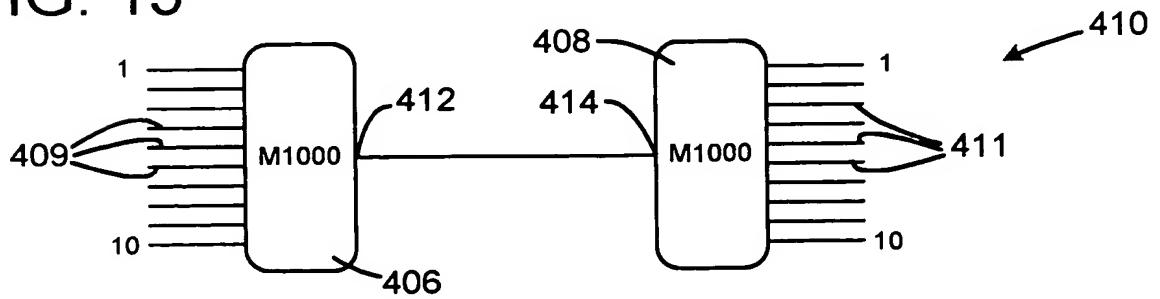


FIG. 15



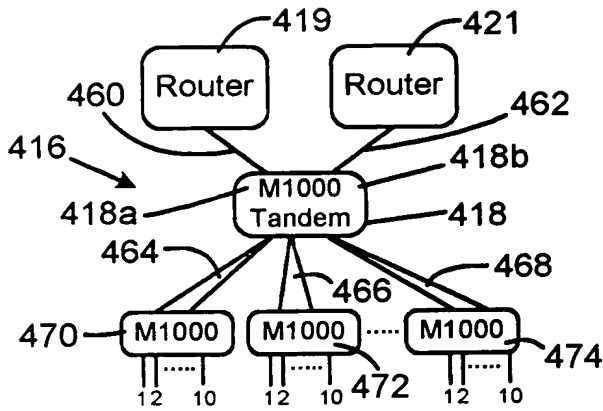


FIG. 16

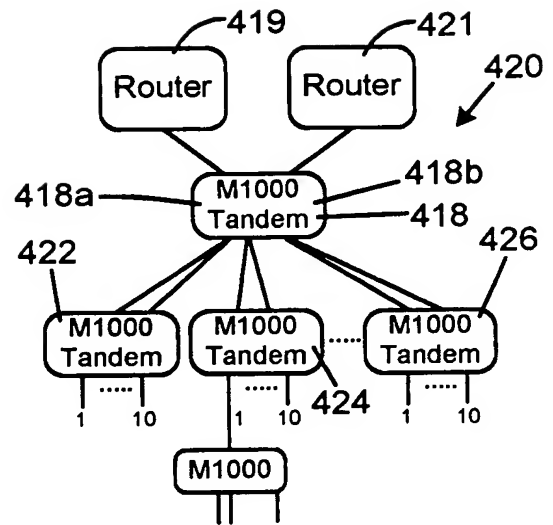


FIG. 17

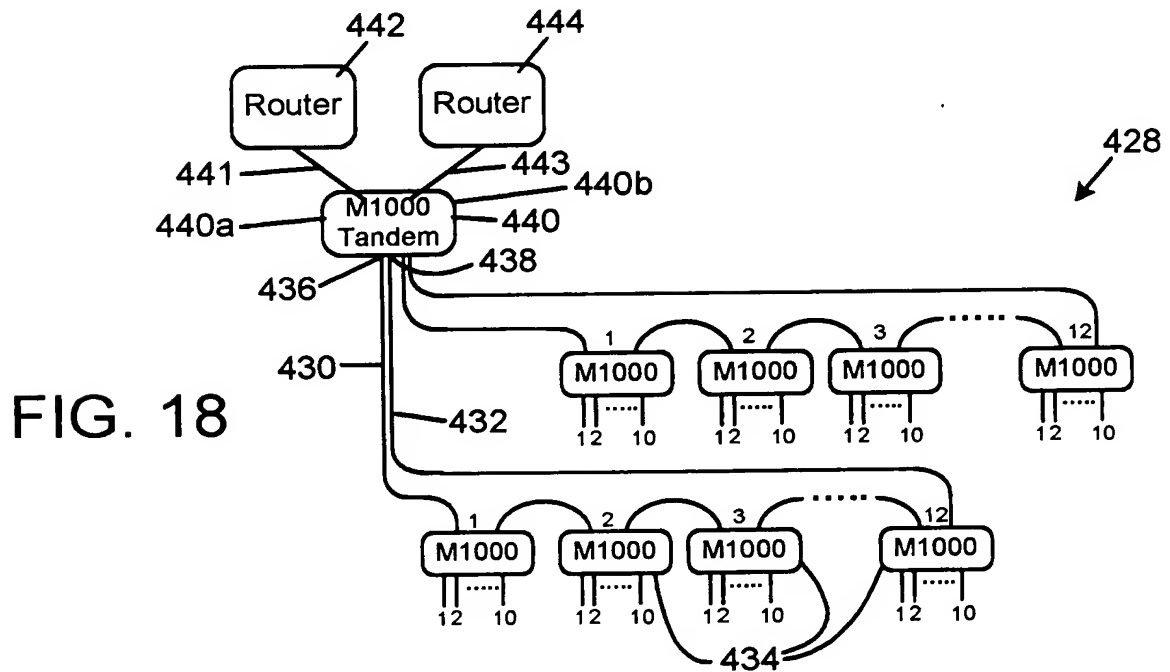


FIG. 18

FIG. 19

